

1c714 U.S. PTO  
09/585427  
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257	59	Subclass
Class	ISSUE CLASSIFICATION	

PATENT NUMBER

6380559



6380559

U.S. UTILITY Patent Application

SCANNED	O.I.P.E. <i>CH</i>	PATENT DATE
G.A. <i>47</i>		APR 30 2002

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/585427	F	257	59	2811 2826	TRAN, M.

APPLICANTS  
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TITLE  
Thin film transistor array substrate for a liquid crystal display and a method for fabricating the same

PTO 2040  
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
257	59	257	72						
INTERNATIONAL CLASSIFICATION		349	106	143	158				
H 0 1 L	29/04								
G 0 2 F	1/136								
G 0 2 F	1/1335								

4/1/02 Formal Drawings (74/510) Set 6/2/00

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg. <i>74</i> Figs. Drwg. <i>76</i> Print Fig. <i>45</i>			<b>CLAIMS ALLOWED</b> Total Claims <i>17</i> Print Claim for O.G. <i>1</i>	
	(Assistant Examiner) _____ (Date) _____ <i>Minh Lee Tran</i> Primary Examiner (Primary Examiner) _____ (Date) <i>12/01</i>			<b>NOTICE OF ALLOWANCE MAILED</b> <i>1/2/02</i>	
	(Legal Counsults Examiner) _____ (Date) <i>1/3/02</i>			<b>ISSUE FEE</b> Amount Due <i>1280.00</i> Date Paid <i>215-02</i>	
				<b>ISSUE BATCH NUMBER</b>	

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